



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
Delegated to Government of Tamil Nadu

B.Tech. Electronics & Communication Engineering Semester End Examinations Time Table May/June - 2025

Regular Courses

Applicable for 2023 Admitted Batch Only

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm

AN: 02.00 pm to 05.00 pm

S.No.	Date	Course Code	Course Name
1	10.05.2025 FN	10211EC114	VLSI Design
		10213IT102	Data Structures
		10213CS102	Data Structures
2	13.05.2025 FN	10212EC127	Low Power VLSI Design
		10212EC110	Robotics and its Application
		10212EC214	Fundamentals of Machine Learning
		10212EC215	Professional Python Programming
		10212EC107	Digital TV Engineering
		10212EC128	VLSI Design Techniques
		10210CH104	Environmental Science and Sustainability
3	15.05.2025 FN	10210MA106	Probability and Random Processes
4	17.05.2025 FN	10211EC109	Microprocessor and Microcontroller
		10213CS101	Object Oriented Programming using Java
		10213IT101	Object Oriented Programming using Java
		10210MA103	Probability, Statistics and Queuing Theory
		10211EC104	Linear Integrated Circuits
5	20.05.2025 FN	10211EC107	Electromagnetics and Transmission Lines
6	22.05.2025 FN	10211EC110	Data Communication Networks
		10213EN105	Academic Writing
		10213CE101	Building Materials
7	24.05.2025 FN	10210MA104	Fourier Series and Transform Techniques
		10211EC106	Signals and Systems
8	27.05.2025 FN	10212EC174	Principles of Data Science
		10212EC104	Radar and Electronic Navigation Systems
		10212EC222	Principles of Networking and Cyber Security
		10212EC117	Network Management
		10212EC116	Network Security
		10213ME105	3D Printing and Tooling
		10212EC226	Machine Learning

S.No.	Date	Course Code	Course Name
9	29.05.2025 FN	10211EC108	Communication Systems
		10211EC102	Analog Electronics
10	31.05.2025 FN	10210ME101	Design Thinking
		10211EC101	Circuit Theory
11	03.06.2025 FN	10210ME104	Project Management and Finance
12	05.06.2025 FN	10211EC103	Digital Electronics
13	09.06.2025 FN	10213CS105	Computer Networks
		10213CS103	Operating Systems

d. Lawer

Savil **Controller of Examinations**

